WHAT IS CLAIMED IS:

| 1 | 1. A device comprising: | | | |
|----|---|--|--|--|
| 2 | an I/O connection adapted to communicate an I/O value and having a | | | |
| 3 | configurable attribute; | | | |
| 4 | a configuration memory adapted to store a first attribute value that configures | | | |
| 5 | the configurable attribute; | | | |
| 6 | a diagnostic interface adapted to communicate the I/O value; and | | | |
| 7 | a diagnostic controller having a first mode adapted to communicate the I/O | | | |
| 8 | value between the I/O connection and the diagnostic interface and having a second mode | | | |
| 9 | adapted to receive the first attribute value from the diagnostic interface and to store the | | | |
| 10 | received first attribute value in the configuration memory, thereby configuring the | | | |
| 11 | configurable attribute of the I/O connection. | | | |
| 1 | 2. The device of Claim 1, wherein the second mode is further adapted to | | | |
| 2 | read a second attribute value previously stored in the configuration memory and to send the | | | |
| 3 | second attribute value to the diagnostic interface, wherein the second attribute value | | | |
| 4 | previously configured the configurable attribute. | | | |
| 1 | 3. The device of Claim 1, wherein the configuration memory is further | | | |
| 2 | adapted to store a second attribute value that configures a second configurable attribute; and | | | |
| 3 | wherein the second mode of the diagnostic controller does not store the second attribute value | | | |
| 4 | in the configuration memory, wherein the second configurable attribute is unassociated with | | | |
| 5 | the I/O connection. | | | |
| 1 | 4. The device of Claim 3, wherein the configuration memory comprises a | | | |
| 1 | I/O configuration memory adapted to store the first attribute value and a core configuration | | | |
| 2 | | | | |
| 3 | memory adapted to store the second configurable attribute value. | | | |
| 1 | 5. The device of Claim 2, wherein the I/O configuration memory | | | |
| 2 | comprises a shift register adapted to shift in and store the first attribute value and to shift out | | | |
| 3 | and output the second attribute value. | | | |
| 1 | 6. The device of Claim 1, wherein the diagnostic interface comprises a | | | |
| 2 | serial data connection. | | | |

7. 1 The device of Claim 6, wherein the serial data connection is adapted to 2 receive a second I/O value from an I/O connection of a second device and to send the second I/O value to a third device. 3 1 8. The device of Claim 7, wherein the diagnostic interface is a JTAG 2 interface. 1 9. The device of Claim 1, further comprising: 2 a configuration interface adapted to receive a set of attribute values for a set of 3 configurable attributes of the device from a configuration device; and 4 a configuration controller adapted to store the set of attribute values in the 5 configuration memory, thereby configuring the set of configurable attributes of the device. 1 10. The device of Claim 9, wherein the set of attribute values include a 2 second attribute value configuring the configurable attribute of the I/O connection; 1 11. The device of Claim 9, wherein the second mode of the diagnostic 2 controller disables the configuration controller. 1 12. The device of Claim 9, wherein the configuration controller is further 2 adapted to receive a signal and to retrieve the set of attribute values in response to the signal. 1 13. The device of Claim 12, wherein the signal is received from a source 2 external to the device. 1 14. The device of Claim 12, wherein the signal is received from the 2 diagnostic controller. 1 15. The device of Claim 14, wherein the diagnostic controller further 2 includes a third mode for receiving a configuration instruction from the diagnostic interface 3 and generating the signal in response to the configuration instruction. 1 16. The device of Claim 15, wherein the diagnostic controller further includes a pulse generator for generating the signal. 2

The device of Claim 1, wherein the device is an integrated circuit.

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| 1 | 18. The device of Claim 1, wherein the device is a programmable logic | | | |
|----|--|--|--|--|
| 2 | device. | | | |
| 1 | 19. The device of Claim 1, further comprising: | | | |
| 2 | s, same of mp. on. g. | | | |
| _ | a system having a plurality of devices connected with the device. | | | |
| 1 | 20. The device of Claim 19, wherein the system further includes a | | | |
| 2 | configuration device. | | | |
| 1 | 21. The device of Claim 1, further comprising: | | | |
| 2 | a circuit board having a plurality of additional devices mounted thereto, such | | | |
| 3 | a proton of additional devices mounted thereto, such | | | |
| 3 | that the device is connected with at least one other device on the circuit board. | | | |
| 1 | 22. The device of Claim 21, wherein the circuit board further includes a | | | |
| 2 | configuration device. | | | |
| | | | | |
| 1 | 23. A device comprising: | | | |
| 2 | an I/O connection adapted to communicate an I/O value; | | | |
| 3 | a set of configurable attributes defining the function of the device; | | | |
| 4 | a configuration memory adapted to store the set of attribute values configuring | | | |
| 5 | the configurable attributes; | | | |
| 6 | a configuration interface adapted to receive the set of attribute values from a | | | |
| 7 | configuration device; and | | | |
| 8 | a configuration controller adapted to store in the configuration memory the set | | | |
| 9 | of attribute values received by the configuration interface in response to a configuration | | | |
| 10 | signal, thereby configuring the set of configurable attributes of the device; | | | |
| 11 | a diagnostic interface adapted to communicate the I/O value of the I/O | | | |
| 12 | connection; and | | | |
| 13 | a diagnostic controller having a first mode adapted to communicate the I/O | | | |
| 14 | value between the I/O connection and the diagnostic interface and having a second mode | | | |
| 15 | adapted to send the configuration signal to the configuration controller. | | | |
| 1 | 24. The device of Claim 23, wherein the diagnostic controller is further | | | |
| 2 | adapted to receive the configuration instruction from the diagnostic interface and to send the | | | |

| 3 | configuration signal to the configuration controller in response to the configuration | | | | |
|---|---|-----------|---|--|--|
| 4 | instruction. | | | | |
| 1 | | 25. | The device of Claim 23, wherein the diagnostic controller further | | |
| 2 | includes a puls | se gener | rator for generating the configuration signal. | | |
| 1 | | 26. | The device of Claim 23, wherein the configuration controller is further | | |
| 2 | adapted to rec | eive the | configuration signal from a source external to the device. | | |
| 1 | | 27. | The device of Claim 23, wherein the device is an integrated circuit. | | |
| 1 | | 28. | The device of Claim 23, wherein the device is a programmable logic | | |
| 2 | device. | | | | |
| 1 | | 29. | A method for configuring an attribute of an I/O connection of a | | |
| 2 | reconfigurable device comprising: | | | | |
| 3 | | receivi | ng a diagnostic instruction from a diagnostic interface; | | |
| 4 | | commu | inicating an I/O value from the I/O connection to the diagnostic | | |
| 5 | interface when | the dia | agnostic instruction is of a first type; | | |
| 6 | | receivi | ng an attribute value associated with the attribute of the I/O connection | | |
| 7 | from the diagr | nostic in | terface when the diagnostic instruction is of a second type; and | | |
| 8 | | storing | the attribute value in a configuration memory, thereby configuring the | | |
| 9 | I/O connection | n, when | the diagnostic instruction is of the second type. | | |
| 1 | | 30. | The method of Claim 29, wherein storing the attribute value | | |
| 2 | comprises: | | | | |
| 3 | | shifting | g the attribute value into a shift register; | | |
| 4 | | shifting | g a previously stored attribute value of the I/O connection out of the | | |
| 5 | shift register; | and | | | |
| 6 | | comm | unicating the previously stored attribute value with the diagnostic | | |
| 7 | interface. | | | | |
| 1 | | 31. | The method of Claim 29, wherein the diagnostic interface comprises a | | |
| 2 | serial data connection. | | | | |
| 1 | | 32. | The method of Claim 29, wherein the diagnostic interface is a JTAG | | |
| 2 | interface | | | | |

| 1 | 33. The method of Claim 29, further comprising: | | | |
|----|---|--|--|--|
| 2 | receiving a configuration signal via the diagnostic interface; | | | |
| 3 | retrieving a set of attribute values defining the function of the reconfigurable | | | |
| 4 | device from a configuration device via a configuration interface in response to the | | | |
| 5 | configuration signal; and | | | |
| 6 | storing the set of attribute values in the configuration memory, thereby | | | |
| 7 | defining the function of the reconfigurable device. | | | |
| 1 | 34. The method of Claim 33, wherein the set of attribute values includes a | | | |
| 2 | | | | |
| 1 | 35. The method of Claim 33, wherein receiving the configuration signal, | | | |
| 2 | retrieving the set of attribute values, and storing the set of attribute values are disabled when | | | |
| 3 | the diagnostic instruction is of the second type. | | | |
| | 2 | | | |
| 1 | 36. A system having a plurality of devices, the system comprising: | | | |
| 2 | a reconfiguration device having a stored set of device attributes; | | | |
| 3 | a reconfigurable device having a set of configurable attributes and adapted to | | | |
| 4 | receive the stored set of device attributes, thereby configuring the reconfigurable device; and | | | |
| 5 | a diagnostic interface adapted to interface with the reconfigurable device and | | | |
| 6 | with an external testing device, thereby communicating an I/O value associated with an I/O | | | |
| 7 | connection of the reconfigurable device to the external testing device; | | | |
| 8 | wherein the reconfigurable device includes a configuration controller adapted | | | |
| 9 | to initiate the reception of the stored set of device attributes in response to a configuration | | | |
| 10 | signal, and a diagnostic controller having a first mode adapted to communicate the I/O value | | | |
| 11 | between the I/O connection and the diagnostic interface and having a second mode adapted to | | | |
| 12 | send the configuration signal to the configuration controller. | | | |
| 1 | 37. The system of Claim 36, wherein the diagnostic controller is further | | | |
| 2 | adapted to receive from the diagnostic interface a second set of device attributes adapted to | | | |
| 3 | configure the reconfigurable device. | | | |
| 1 | 38. The system of Claim 36, wherein the diagnostic controller is further | | | |
| 2 | adapted to send the configuration signal to the configuration controller in response to a | | | |
| 3 | configuration instruction received from the diagnostic interface. | | | |

- 1 39. The system of Claim 36, wherein the configuration controller is further
- 2 adapted to receive the configuration signal from a source external to the device.
- 1 40. The system of Claim 36, wherein the diagnostic interface is a JTAG
- 2 interface.